

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: David E. McKinley
 Assignee: Zilog, Inc.
 Title: "DYNAMIC POWER MANAGEMENT OF SOLID STATE MEMORIES"
 Serial No.: 08/368,577 Filed: January 4, 1995
 Examiner: G. Davis Group Art Unit: 2309

Attorney Docket No.: M-2689-1C US

San Jose, California
 July 26, 1996

COMMISSIONER OF PATENTS AND TRADEMARKS
 Washington, D. C. 20231

AMENDMENT

RECEIVED

AUG 15 1996

COMMUNICATIONS

Sir:

In response to the Office Action of February 26, 1996 please amend the above identified application as set forth below.

IN THE CLAIMS

Sub C1 1. (Thrice Amended) A dynamic power management device for supplying power to a solid state memory integrated circuit [in an electronic system], said device comprising:

power control means for supplying a variable voltage to said memory integrated circuit [without varying a voltage being supplied to elements of said electronic system other than said memory integrated circuit]; and

logic control means for generating address and control signals for said memory integrated circuit and for controlling said power control means to supply power to said memory integrated circuit at a level to maintain memory information in said memory integrated circuit during periods of no data access activity, and [supplying] to supply power at a level to read and write memory information in said memory integrated circuit during

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